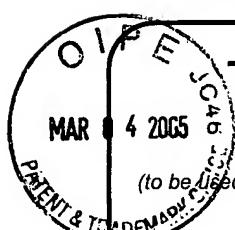


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# TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

	Application Number	10/666,742
	Filing Date	September 19, 2003
	First Named Inventor	Wood et al.
	Group Art Unit	2812
	Examiner Name	O. Luk
	Attorney Docket Number	2269-6095US

## ENCLOSURES (check all that apply)

<input checked="" type="checkbox"/> Postcard receipt acknowledgment (attached to the front of this transmittal) <input checked="" type="checkbox"/> Duplicate copy of this transmittal sheet in the event that additional filing fees are required under 37 C.F.R. § 1.16 <input type="checkbox"/> Preliminary Amendment <input checked="" type="checkbox"/> Preliminary Amendment and Response to Restriction Requirement dated February 14, 2005 <input type="checkbox"/> Amendment in response to office action dated <input type="checkbox"/> Amendment under 37 C.F.R. § 1.116 in response to final office action dated <input type="checkbox"/> Additional claims fee - Check No. in the amount of \$ <input type="checkbox"/> Letter to Chief Draftsman and copy of FIGS. with changes made in red <input type="checkbox"/> Transmittal of Formal Drawings <input type="checkbox"/> Formal Drawings ( sheets)	<input type="checkbox"/> Information Disclosure Statement, PTO/SB/08A; <input type="checkbox"/> copy of cited references <input type="checkbox"/> Supplemental Information Disclosure Statement; PTO/SB/08A; copy of cited references and Check No. in the amount of \$180.00 <input type="checkbox"/> Associate Power of Attorney <input type="checkbox"/> Petition for Extension of Time and Check No. in the amount of \$ <input type="checkbox"/> Petition <input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Other Enclosure(s) (please identify below):
Remarks		
The Commissioner is authorized to charge any additional fees required but not submitted with any document or request requiring fee payment under 37 C.F.R. §§ 1.16 and 1.17 to Deposit Account 20-1469 during pendency of this application.		

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Brick G. Power	Registration No. 38,581
Signature		
Date	February 28, 2005	

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

Typed or printed name	Rachael M. Harris
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Date	February 28, 2005

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PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:** Wood et al.

**Serial No.:** 10/666,742

**Filed:** September 19, 2003

**For:** METHODS FOR THINNING  
SEMICONDUCTOR SUBSTRATES THAT  
EMPLOY SUPPORT STRUCTURES  
FORMED ON THE SUBSTRATES  
(amended)

**Confirmation No.:** 6057

**Examiner:** O. Luk

**Group Art Unit:** 2812

**Attorney Docket No.:** 2269-6095US  
(03-0593.00/US)



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February 28, 2005  
Date

Rachael M. Harris  
Signature

Rachael M. Harris  
Name (Type/Print)

**PRELIMINARY AMENDMENT AND RESPONSE TO RESTRICTION  
REQUIREMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This Preliminary Amendment and Response is being filed in response to the Restriction Requirement dated February 14, 2005, the thirty-day shortened statutory period for response to which is set to expire on March 16, 2005.

**An amendment to the title** appears on page 2 of this paper;

**Amendments to the specification** begin on page 3 of this paper;

**Amendments to the claims** are set forth in the listing of the claims, which begins on page 16 of this paper;

**Corrections to the drawings** are identified on page 19 of this paper; and

**Remarks** start at page 20 of this paper.

IN THE TITLE:

Please amend the title as follows:

~~SUPPORT STRUCTURE METHODS FOR THINNING SEMICONDUCTOR  
SUBSTRATES AND THINNING METHODS EMPLOYING THE THAT EMPLOY~~  
SUPPORT STRUCTURES FORMED ON THE SUBSTRATES